Features

- implementation of the Basic CAN specification
- no generated Overload Frames
- receiving and transmitting of both identifiers (CAN specification 2.0B)
- programmable data rate up to 1 Mbit/s
- programmable baud rate prescaler (up to 1/30)
- complete synthesizability
- application specific interface to the host-controller
- link to commercial bus drivers (for instance PCA82C250T by Philips)
- with Bosch-reference model verificate
- the macrocell include about 6500 gates.

Application

The HDL-model can translate to the schematic with a available standardcell library. So a chip can supplement with a full CAN-interface.

A CAN bus is possible with a driver customary in trade.

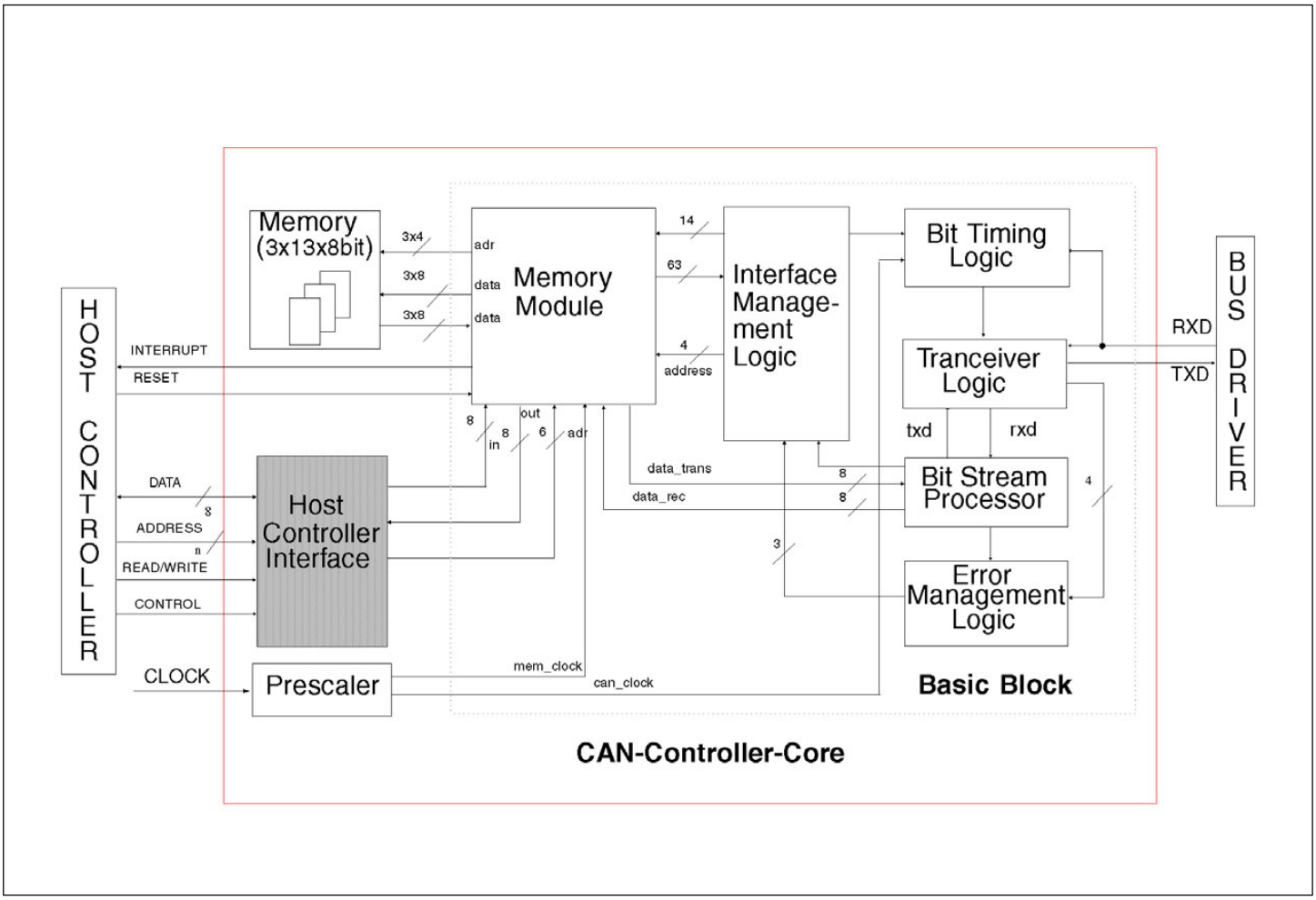

Fig. 3: Block diagram